



# Microfarbication by Micromachining and Deposition





# **FIB Applications I**

Principle of micromachining and deposition ≻Mask repair ► IC chip modification Semiconductor failure analysis ➢ 3D microfabrication > 2D nanofabriaction





# Principle of micromachining and deposition

Etch rate =  $\frac{YMm_{p}I}{A \rho e}$ 



Y : yield rate of ion M : atomic mass of solid surface m<sub>p</sub> : proton mass A : area exposed to beam ρ : density of solid surface I : beam current e : electron charge





#### Beam angle, scan rate

**Imaging:** Secondary electrons or ions produced by the incoming Ga<sup>+</sup> beam are collected to form an image of the sample.

**Milling/Etching:** FIB can locally etch the sample surface with submicron precision. Many variables and material properties affect the sputtering rate of a sample. These include beam current, sample density, sample atomic mass, and incoming ion mass. Ga<sup>+</sup> ion sources are the most widely used in commercially available FIBs.





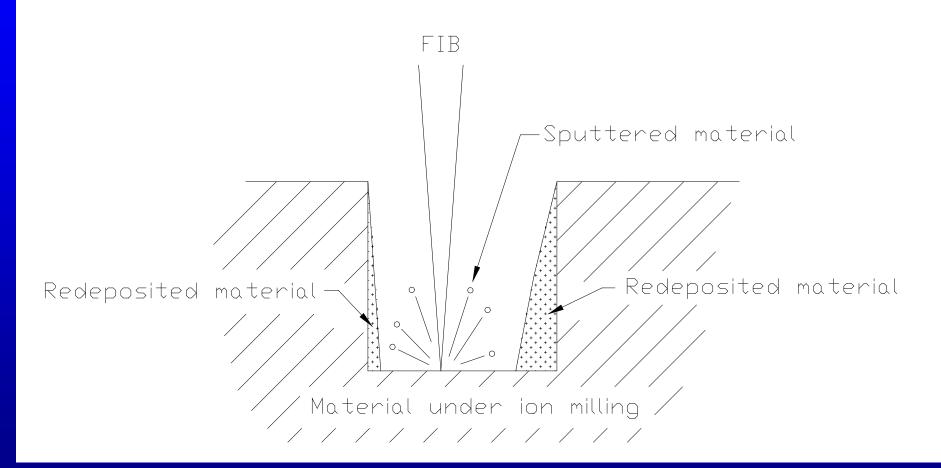
**Gas Assisted Etching:** When a gas is introduced near the surface of the sample during milling, the sputtering yield can increase depending on the chemistry between the gas and the sample. This results in less redeposition and more efficient milling.

**Deposition:** Conductive or insulator material can also be deposited with the aid of a gas in close proximity to the sample surface. Metals often deposited are either Pt or W, while insulators are usually  $SiO_2$  or  $TiO_2$ .





# **Redeposition effect: a negative issue**



### **Redeposition effect of ion beam** micromachining





# Mask repair

Opaque and transparent repair



#### Structural Modification for Circuit Edit and Photomask Repair





# Method for mask repair Deposition

# Ion beam deposition principle

Chemical gas is decomposed under the ion beam sputtering process, and deposited on a defined area with certain defined shape.



#### X-ray Mask Repair

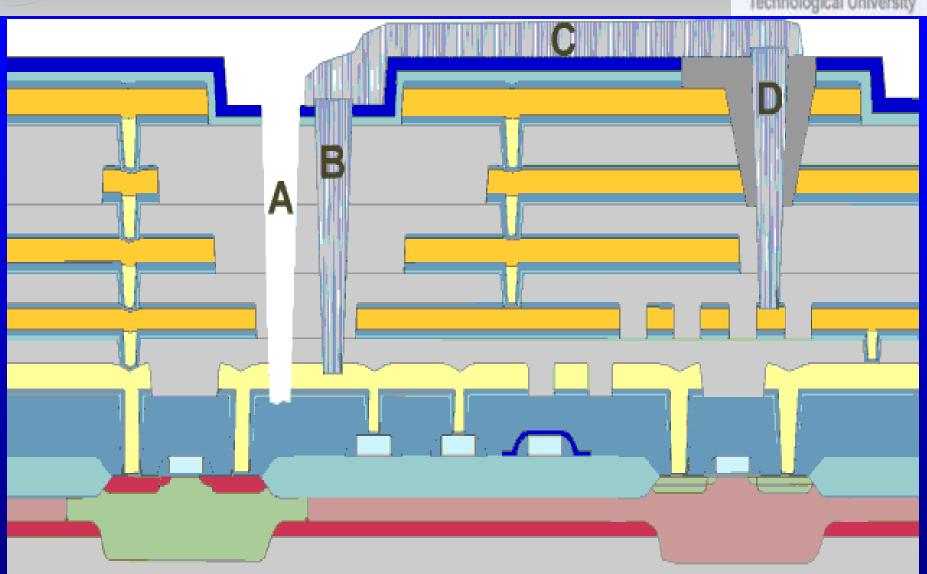




<u>50 µm</u>

SI frame

# **PEN** IC Chip Modification by FIB



Nanyang

Schematic diagram of multi-layer IC chip modification

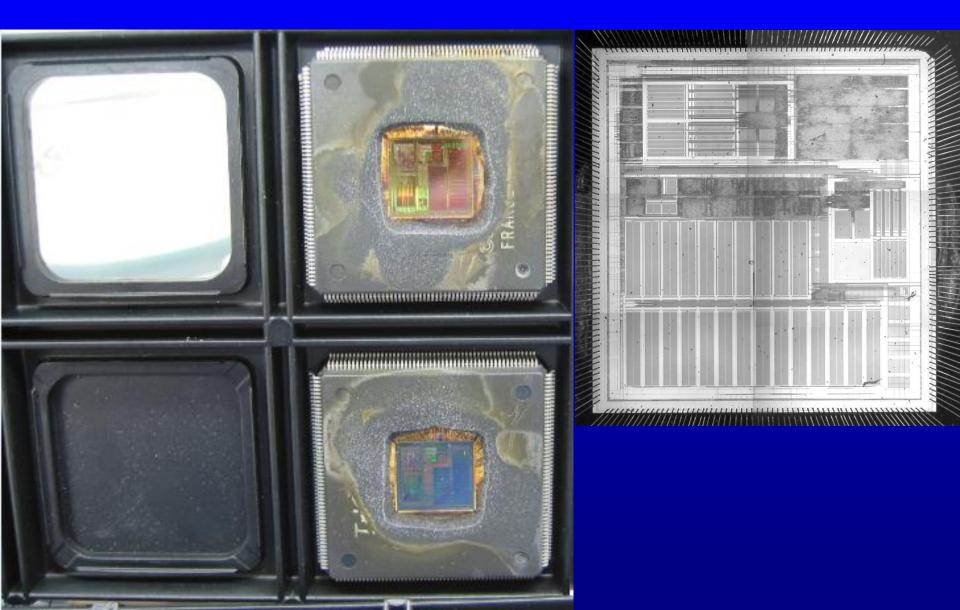


# Two questions for the process:

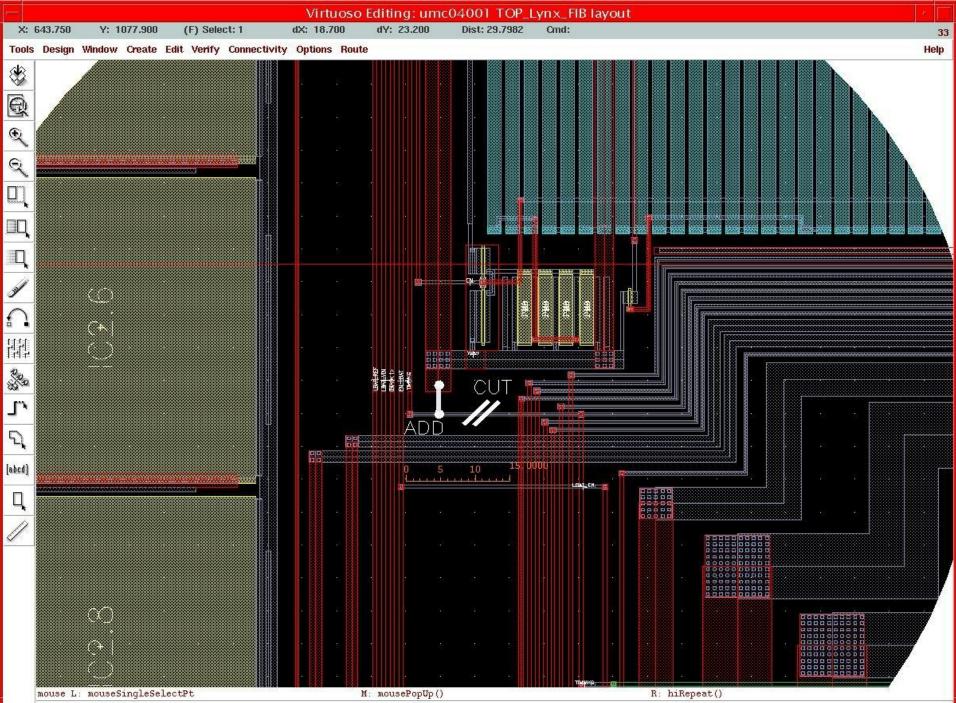
 How to accurately find the cutting or joining location?
How to know the end point from chip layout?







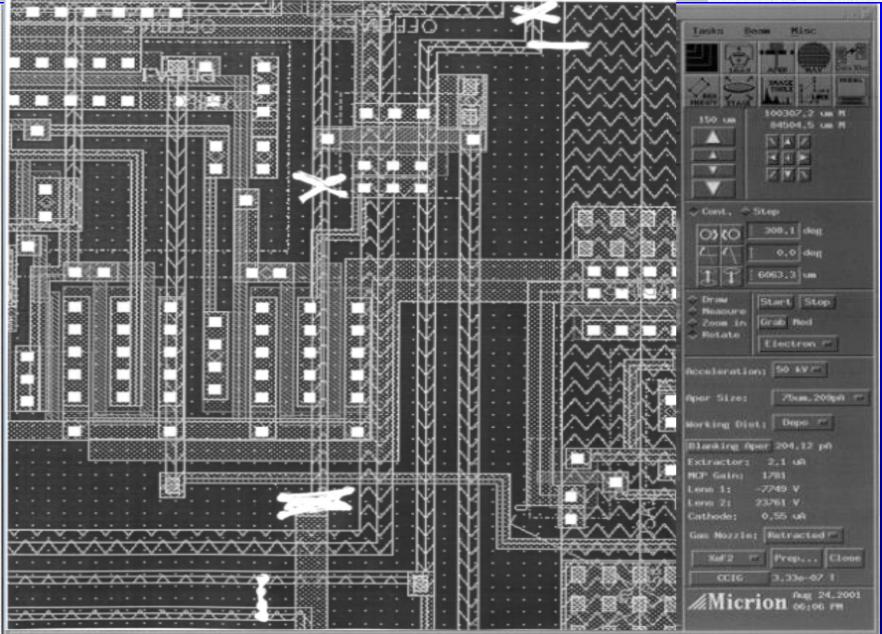
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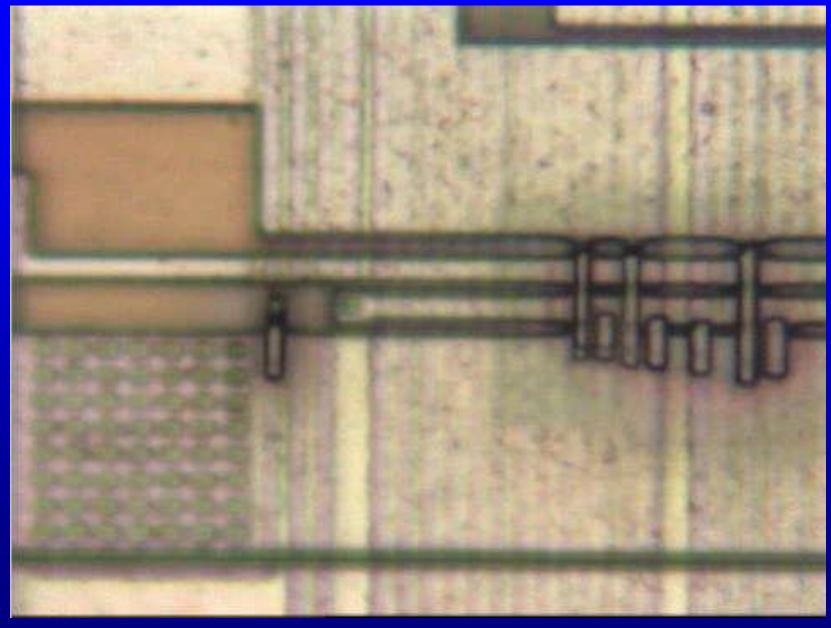






# **Completed IC FIB modification**

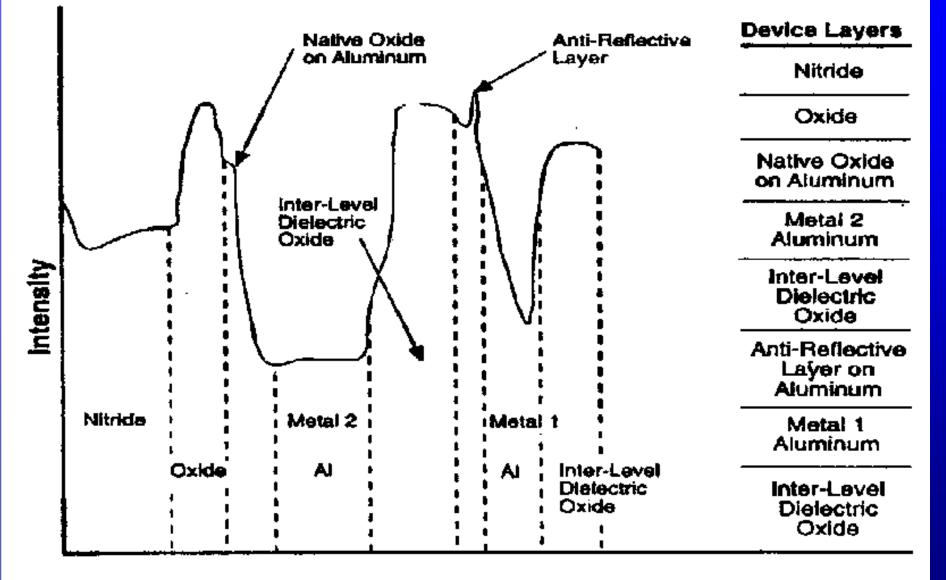






# **End Point Detection**



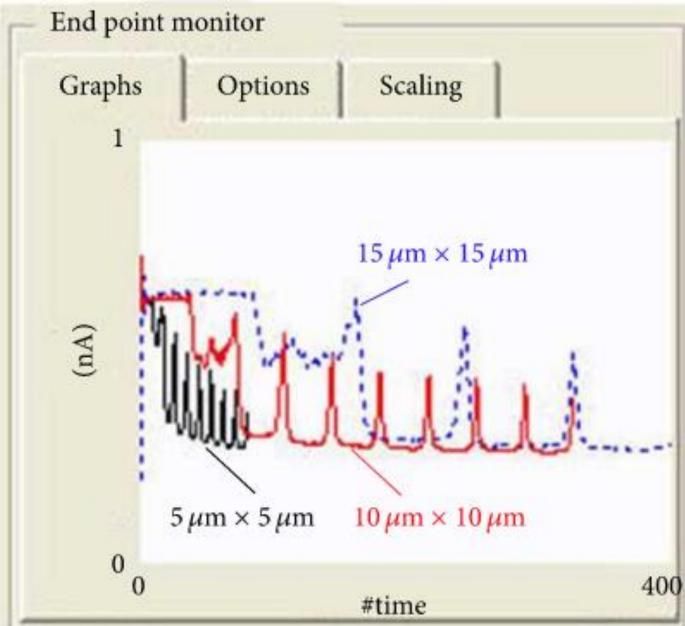


Dose Per Area (nanocoulombs per square micron)



# **End Point Detection**



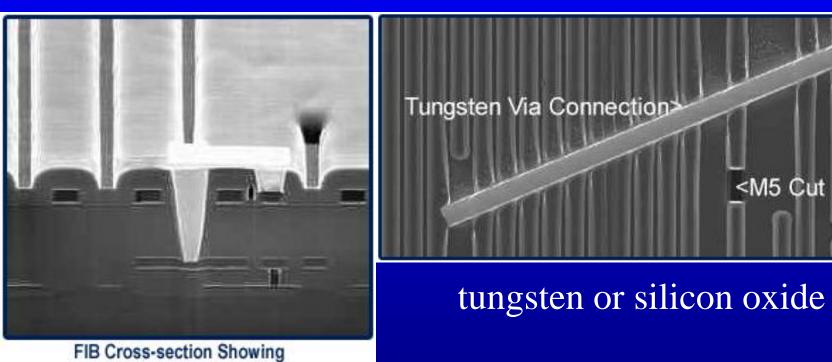






## **IC chip modification**

**Device Modification** 

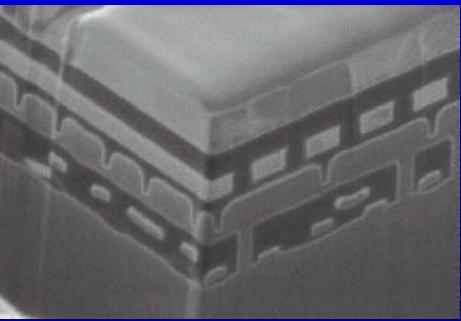






**Rapid IC - Prototyping & Failure Analysis** 

- FIB technology allows for fast failure analysis and circuit modification.
- FIB provides new access to prototyping of microsystems.
- Helps to shorten time-to-market for company ASICs.



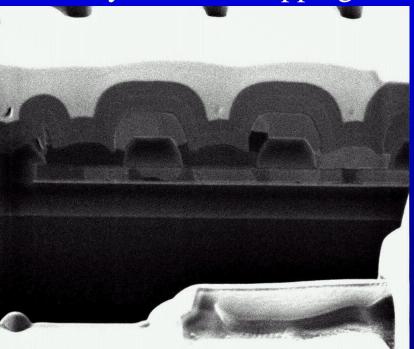
Local cross sectioning of an integrated circuit





#### **Advanced Focused Ion Beam Technology Provides:**

- Local material removal on submicron scale
- Local deposition of conducting and insulating layers in direct writing mode
- In situ processing by high resolution secondary electron mapping



#### Local cross sectioning



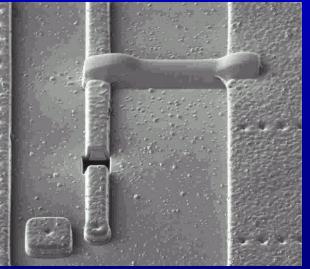


**Fast Verification of Design Changes:** New device does not work properly. Rapid correction of the design and verify its function before starting a new process is required;

Using the redesign data, within a few hours, FIB modify the circuit by:

- Cutting metal lines by FIB milling or gas enhancend etching
- Rewiring of the device by FIB deposition of new metal interconnects

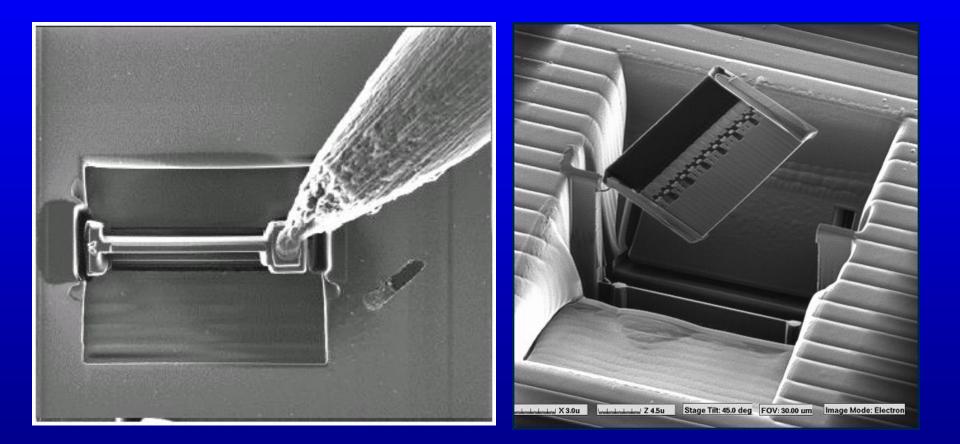
Circuit modification by FIB







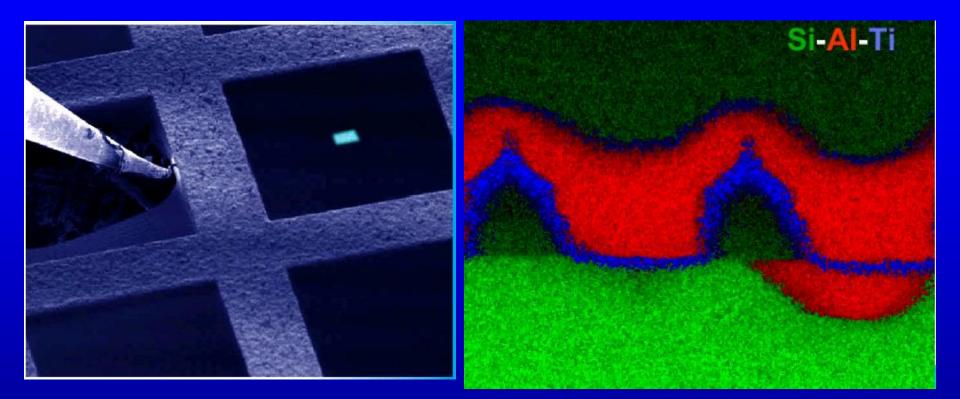
# **Semiconductor failure analysis**



#### **TEM sample preparation via FIBM**





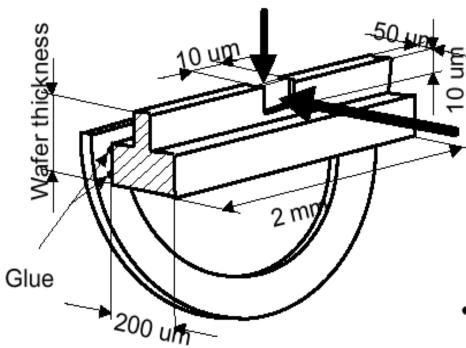


## Transporting a lamella and dispose it onto the grid





# FIB TEM sample preparation by bar sawing.





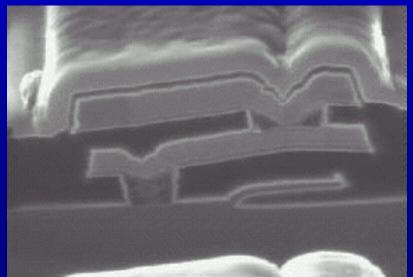
- Re-thinning possible
- High succes rate
- 2 mm bar needed
- Shadowing of X-ray detector





#### **Failure Analysis in Multilayer Metallization:**

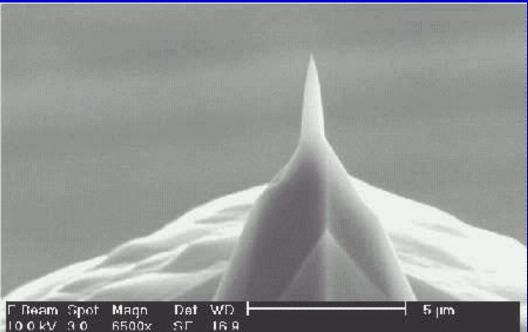
- Your electrical tests indicate malfunction of your interconnects.
- Using local cross sectioning and in situ SE (Secondary Electon Imaging) inspection exactly at the area of interest, the nature of the failure is identified, *e.g.*voids, cracks, particles, *etc*.
- Fabrication of thin lamella by FIB allows TEM (Transmission Electron Microscopy) and high resolution EDX (Electron Diffraction X-ray Spectroscopy) analysis at well defined areas.







- Access to buried metal lines for electrical testing.
- Create test pads by local removal of the passivation above metal lines or by deposition of new test pads.
- Integration of existing optional elements on the chip: Different device options already realized for circuit optimisation are activated/deactivated by FIB.







• Fine tuning of analog devices:

Passive device elements are trimmed by FIB.

• Prototyping of microsystems:

Micromachining of actuators, sensors, and microoptics,

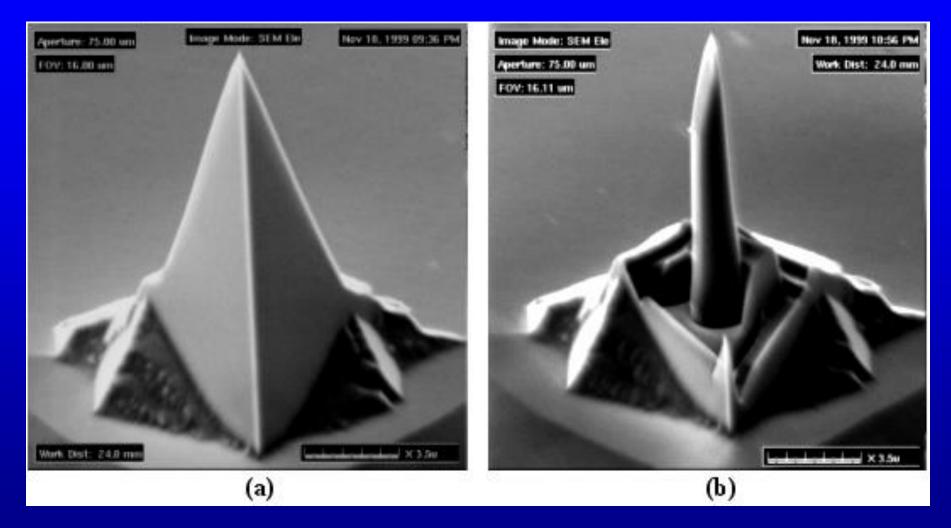
e.g. fabrication and polishing of optical elements,

integration of sensors, fine tuning of cantilevers and tips.



#### Micro-part trimming

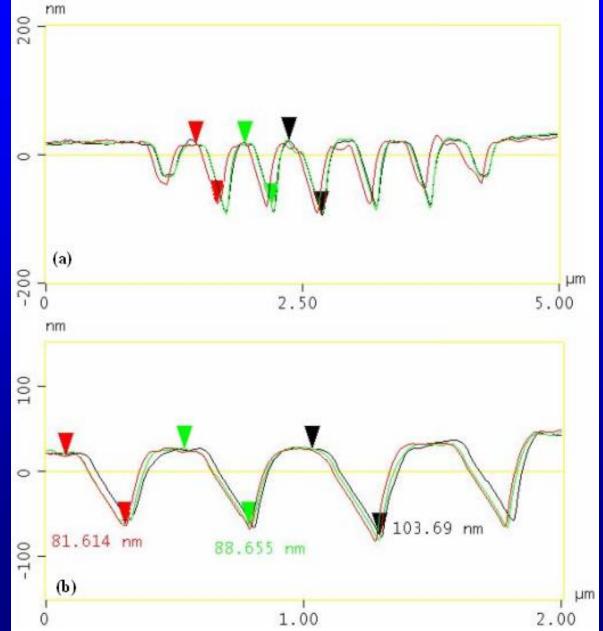




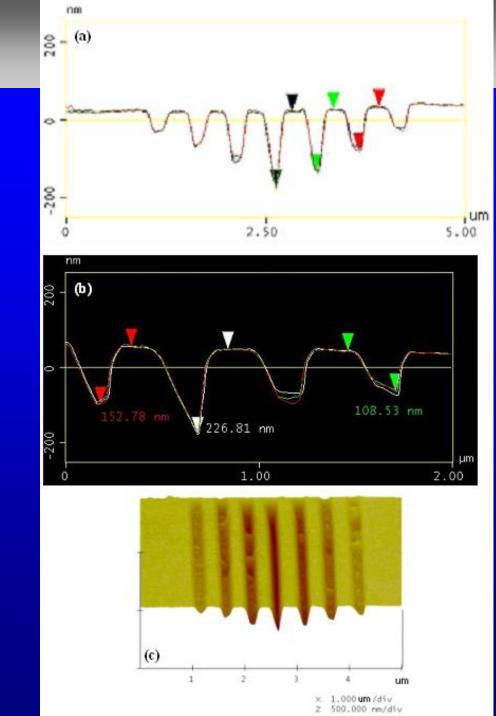
#### AFM tip trimming by FIB fine milling









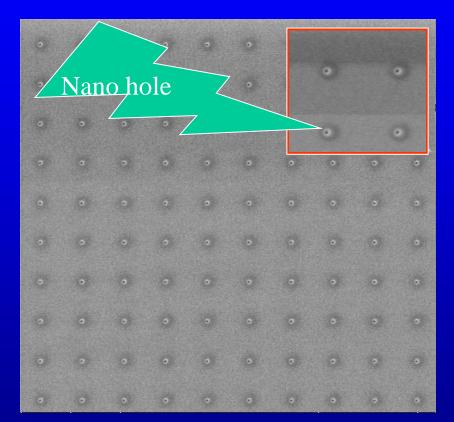




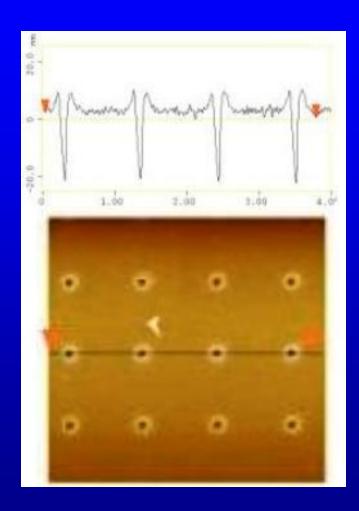


#### Nano-dots writing for data storage





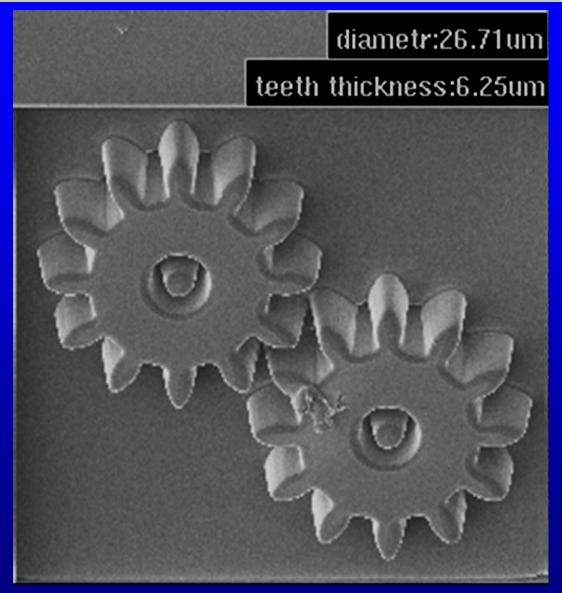
Nano hole array



#### hole diameter $\approx 50$ nm



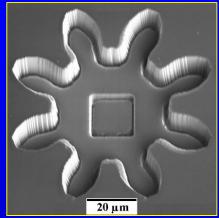




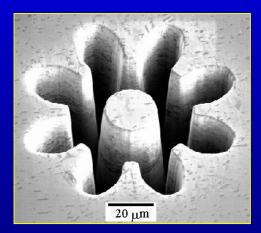
#### Gear with 26.71µm pitch diameter





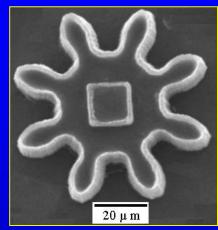


#### **Microcavity on Silicon**









#### **Molded HDPE Microgear**



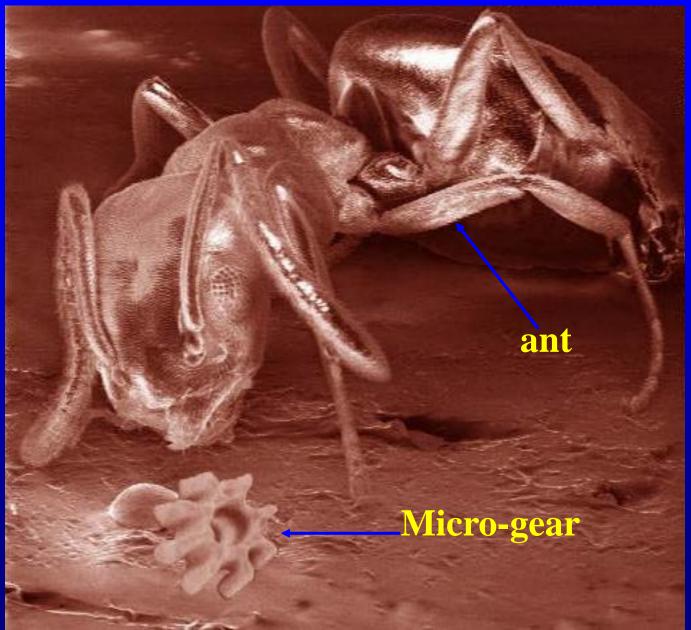
#### **Microcavity on Ni-Be**

#### **Molded HDPE Microgear**

3D micro-cavity milling for master replication use

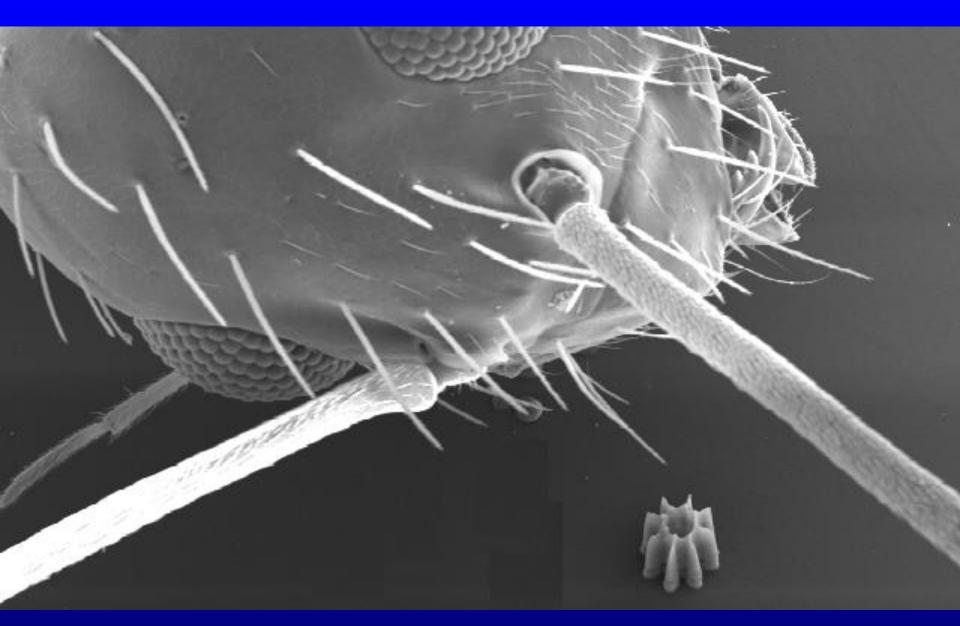


















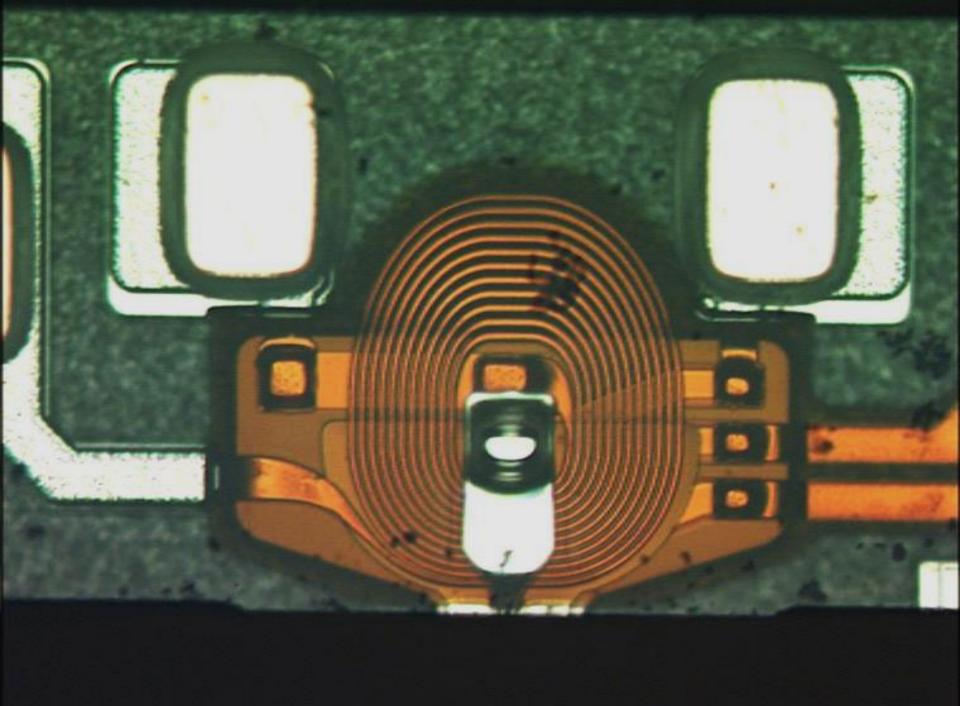
## Sample8: microsurgery tool





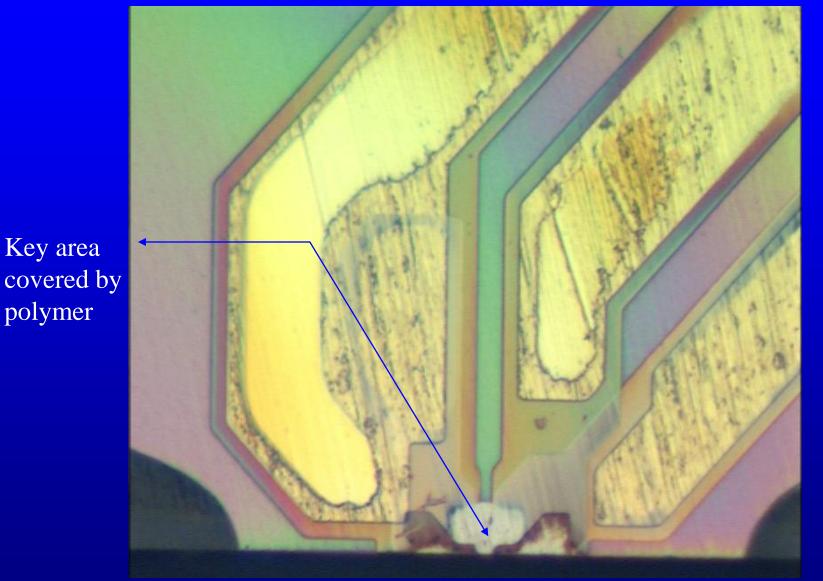


Dragon pattern milled on human hair with diameter of **60 µm** by FIB technology





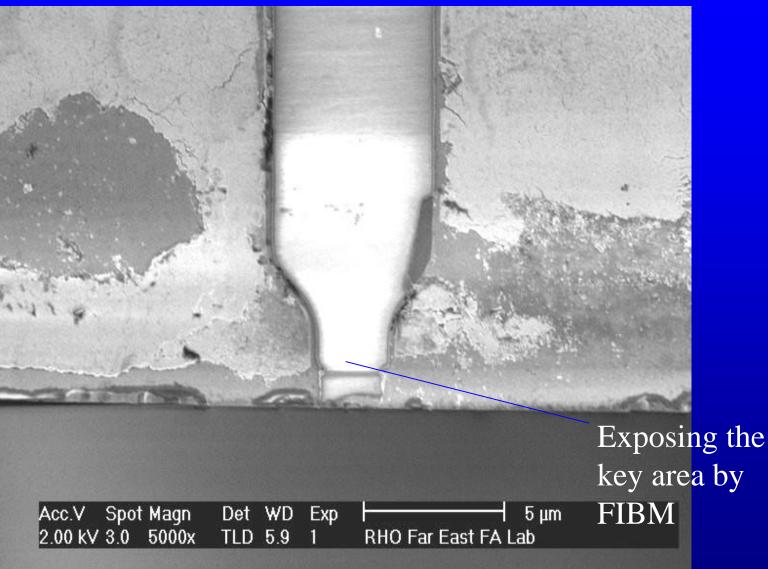




## MR head failure analysis before FIB process



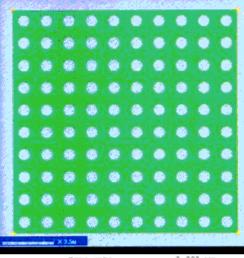


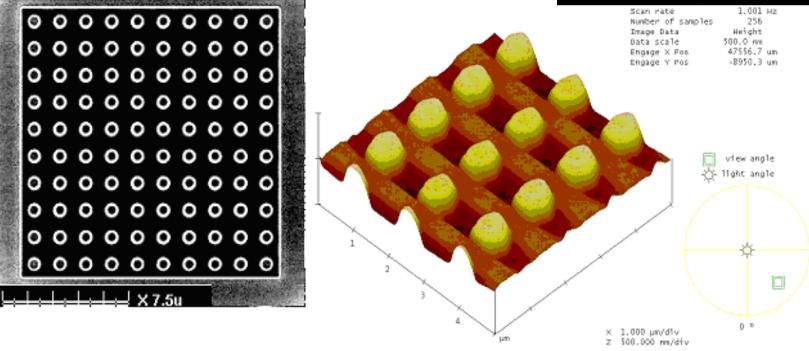


### MR head failure analysis after FIB fine milling





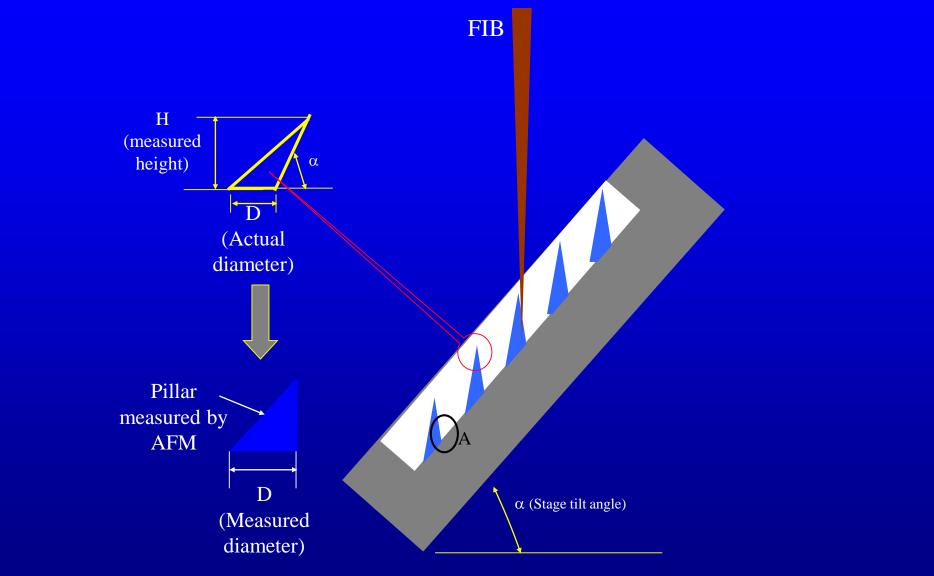




array1-5.001

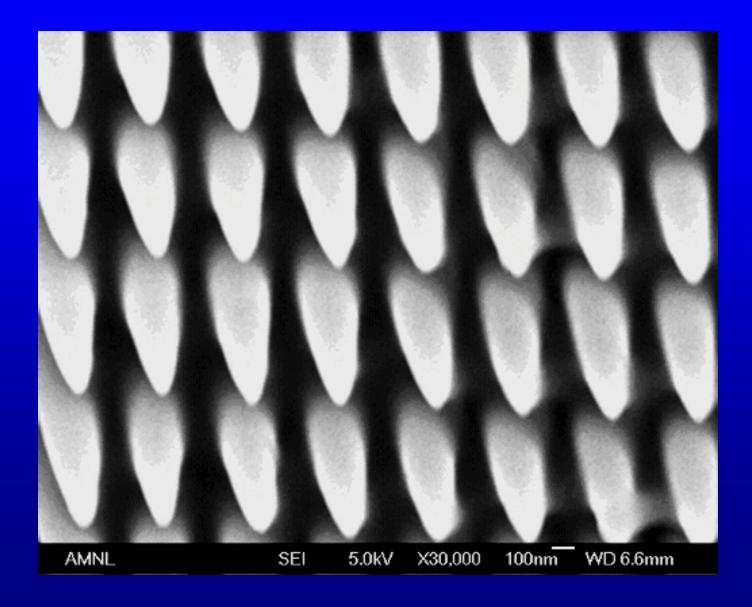






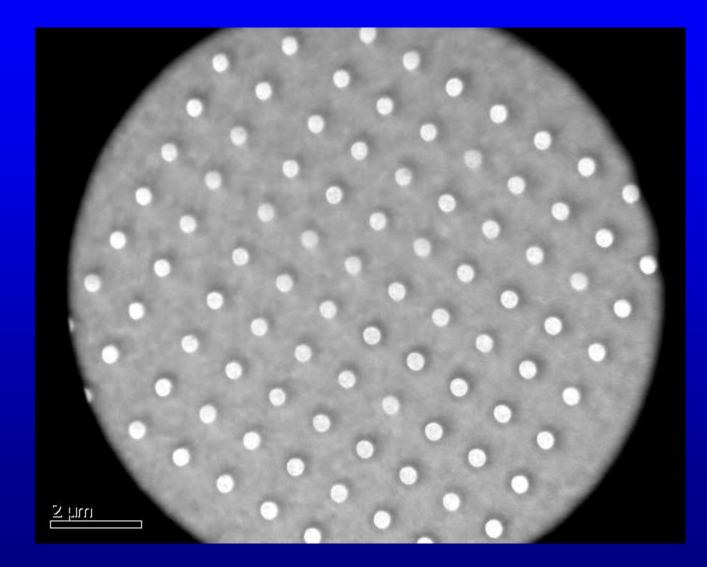






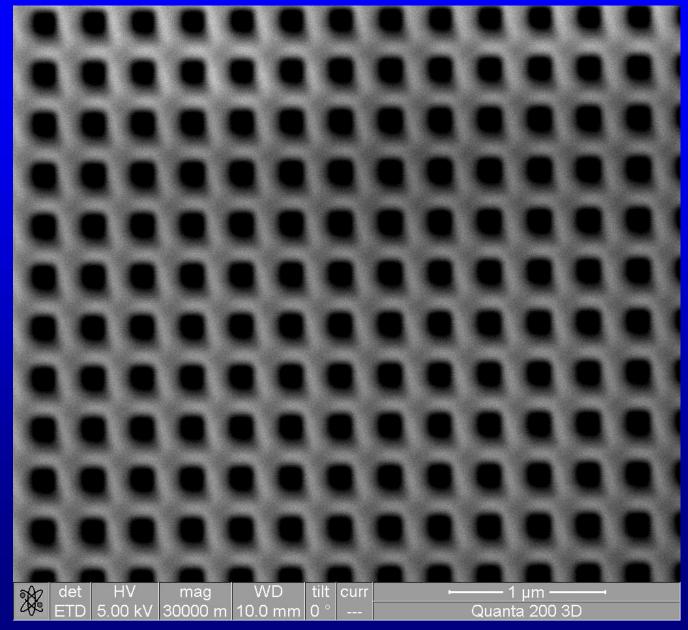
















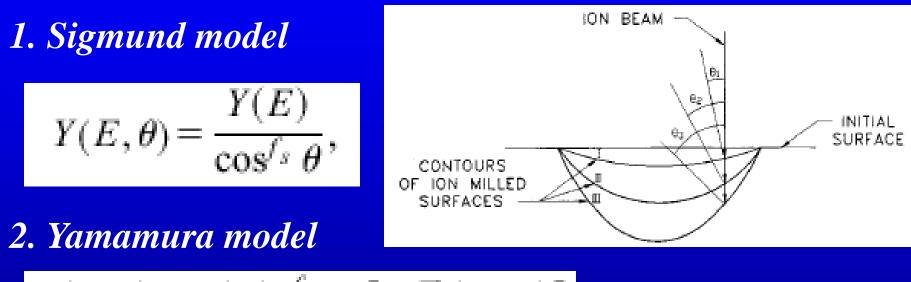
## Theoretical issues

# Etch rate = $\frac{YMm_pI}{A\rho e}$





## Mathematical models of sputter yield



$$Y(E,\theta) = Y(E)t' \exp[-\Sigma(t-1)],$$

Michael J. Vasile, Jushan Xie, and Raji Nassar, JVSTB 17(6), 3085 (1999).

PEN Centre



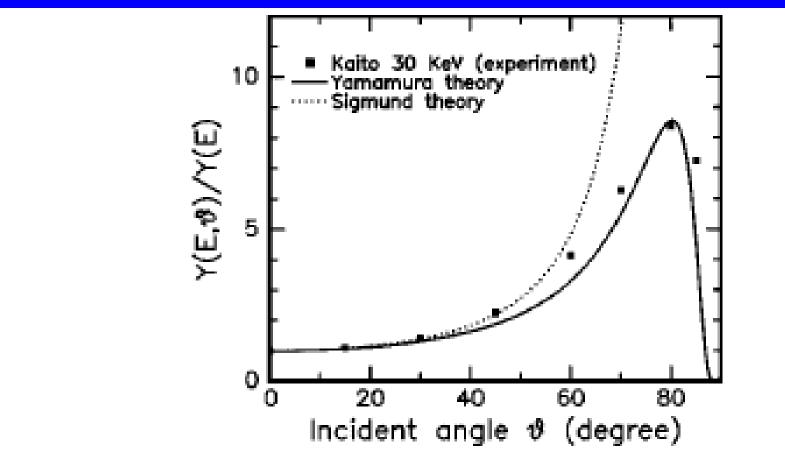


FIG. 2. Normalized sputtering yield of Ga<sup>+</sup>/Si at 30 keV. The points are experimental data measured by Kaito (Ref. 18). The dotted line is calculated from the Sigmund's theory and the solid line from the Yamamura theory. All data are normalized to the sputter yield at  $\theta=0^{\circ}$ .





## 3. Orloff model (normal incidence)

$$Y = 96.4 \quad \frac{\rho Y_{\gamma}}{m}$$

### where

*m* is the mass in the sputtered volume and  $\rho$  is the density in g/cm<sup>3</sup>,  $Y_r$  is sputter rates,





## Table: Sputter rates and sputter yields for selected elements

Element	Symbol	Density	Sputter rates	Sputter yields
		(g/cm <sup>3</sup> )	(µm <sup>3</sup> /nC)	(atoms/ion)
Carbon	C (diamond)	3 <i>5</i> 7	0.18	2.73
Aluminium	Al	2.7	0.3	2.89
Silicon	<u>Si</u>	2 <i>.</i> 33	0.27	2.08
Titanium	Ti	45	0.37	3.35
Chromium	Cr	7.19	0.09	1.20
Zinc	Zn	7.13	0.34	3.57
Germanium	Ge	5 <i>3</i> 2	0.22	1.55
Selenium	Se	4.81	0.43	2.52
Molybdenum	Mo	10.2	0.12	1.32
Silver	Ag	10.5	0.42	3.94
Tin	Sn	5.76	0.25	1.17
Tungsten	W	19.25	0.12	1.22
Platinum	Pt	21.47	0.23	2.44

J. Orloff (Ed.), Handbook of Charged Particle Optics, Boca Raton, Fla: CRC Press, 1997.





## **Estimate the ion dose require**

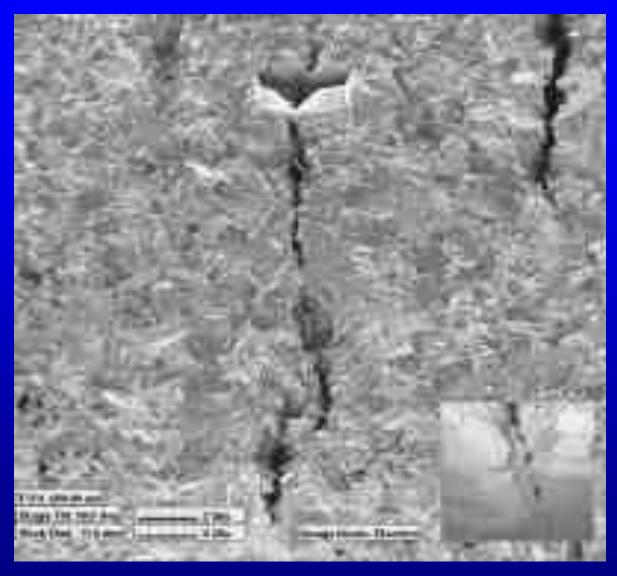
$$d = n \frac{I_b t_d}{e(\delta_d)^2}$$

## where

 $I_b$  is the ion beam current,  $t_d$  is the beam dwell time,  $\delta_d$  is the beam step size and *n* is the number of scans.







**Study of Stress Corrosion Cracking in a Pipeline Steel**